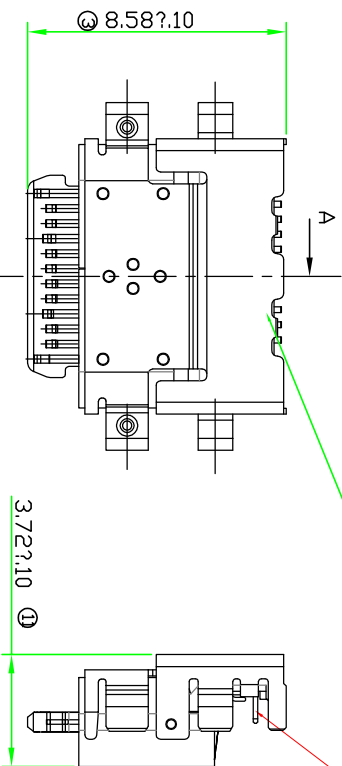
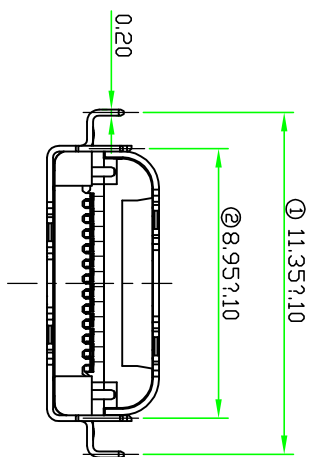
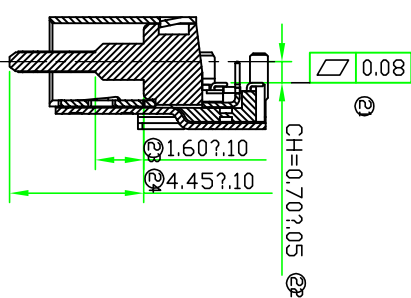
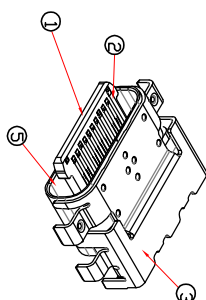
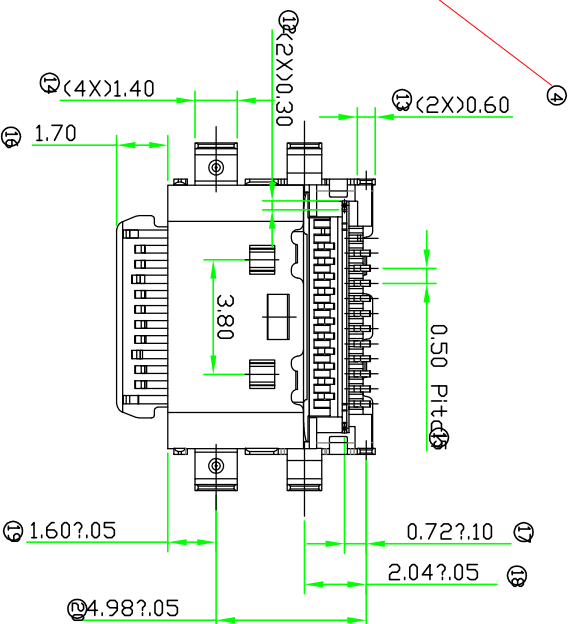
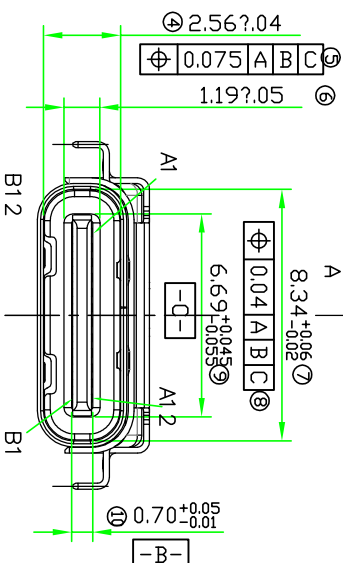


ROHS 客户图

目錄 MAPX A0	變更內容 MODIFICATION NEW	日期 DATE 09-10-20
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DATE CODE
YY MM DD X
物料 15 01 22 01
2015年1月22日 第一條線
生產日期
生產地點
生產廠號



剖面 A-A

5	INTERNAL SHELL	SUS304-1/2H	SOLDERABLE NI 50u" Min. DVER ALL	1
4	MID-PLATE	SUS301-H	SOLDER AREA/G/FINI 50u"Min. DVER ALL.	1
3	SHELL	SUS304-1/2H	SOLDERABLE NI 50u" Min. DVER ALL	1
2	CONTACT	C7035-TM06	CONTACT AREA/AU 30u" Min./SOLDER AREA/G/FINI 50u"~150u". DVER ALL.	12
1	HOUSING	PA46, HFX31S	BLACK/LUL 94V-0	1
	ITEM DEFINITION	MATERIAL	FINISH	Q'TY

产品图

PRODUCT CHART DWG
Shenzhen xinfenglei Electronic Co., Ltd

公差一览表 TOLERANCE UNLESS OTHERWISE		单位 UNITS		制图 DRAWING		产品料号 PRODUCT PART NO.	
X	±0.30	X'	±5'	王冠	00303-UCAF003-X	TYPE C 24P母座双排SMD间距2.28 CH=0.7 L=8.58	
X	±0.25	X'	±2'	审核 CHKD	雷永健	VERSION	
.XX	±0.30	.XX'	±1'	核准 APPD	雷永健	版本 VER	A0
.XXX	±0.15	.XXX'	±0.5'	日期 DATE	2009/10/20	图法 VIEW	

1 2 3 4 5 6 7 8 9 10

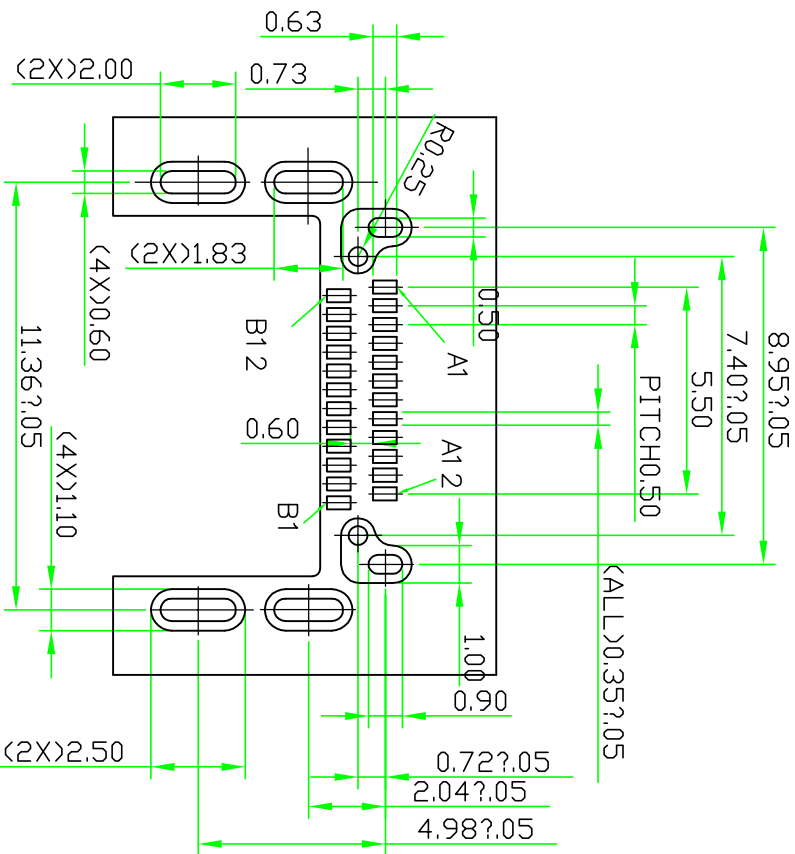
ROHS

客户图

目錄
MAPX
A0

變更內容
MODIFICATION
NEW

日期
DATE
09-10-20



RECOMMENDED PCB LAYOUT(TOL:±0.05)

Pin Assignments

Pin	Signal Name	Description	Mating Sequence	Pin	Signal Name	Description	Mating Sequence
A1	GND	Ground return	First	B12	GND	Ground return	First
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	Second	B11	SSTRXp1	Positive half of first SuperSpeed RX differential pair	Second
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	Second	B10	SSTRXn1	Negative half of first SuperSpeed RX differential pair	Second
A4	VBUS	Bus Power	First	B9	VBUS	Bus Power	First
A5	CC1	Configuration Channel	Second	B8	SBU2	Sideband Use	Second
A6	Dp1	Positive half of the USB2.0 differential pair-Position 1	Second	B7	Dn2	Negative half of the USB2.0 differential pair-Position 2	Second
A7	Dn1	Negative half of the USB2.0 differential pair-Position 1	Second	B6	Dp2	Positive half of the USB2.0 differential pair-Position 2	Second
A8	SBU1	Sideband Use	Second	B5	CC2	Configuration Channel	Second
A9	VBUS	Bus Power	First	B4	VBUS	Bus Power	First
A10	SSTRXn2	Negative half of second SuperSpeed RX differential pair	Second	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair	Second
A11	SSTRXp2	Positive half of second SuperSpeed RX differential pair	Second	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair	Second
A12	GND	Ground return	First	B1	GND	Ground return	First

说明:

1. 电气性能

- 1-1. 额定电流: 3A;
- 1-2. 接触阻抗: 40mΩ最大;
- 1-3. IR test: 500VDC, 0.1S, 50MQ;
- 1-4. HI pot test: 300VAC, 0.1S, 0.5mA;

2. 机械性能

- 2-1. 插入力: 5~20N;
- 2-2. 拔出力: 8~20N(未插过), 6~20N(1000g 次耐久测试后);
- 2-3. 测试速度: 200±50次/小时, 测试10000次;
3. 标示“▲”尺寸必须记录实际检验数据, 标示“▲”的尺寸重点确认;
4. 本产品使用物料及制程须符合“QW-Q-043”文件对应环境管理物质及RoHS2.0&HF要求;

产品图

PRODUCT CHART DWG

公差一览表

TOLERANCE UNITS OTHERWISE	UNIT	SCALE	DATE	DATE	DATE	DATE	DATE	DATE	DATE
X	±0.30	X	±5°						
X	±0.25	X	±2°						
XX	±0.30	XX	±1°						
XXX	±0.15	XXX	±0.5°						

深圳市鑫风雷电子有限公司

Shenzhen xinfenglei Electronic Co., Ltd

单位: MM

比例: 1:1

日期: 2009/10/20

日期: 2009/10/20

日期: 2009/10/20

日期: 2009/10/20

日期: 2009/10/20

日期: 2009/10/20