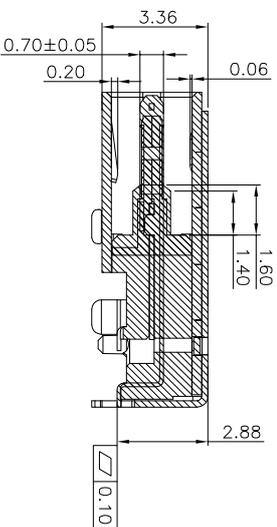
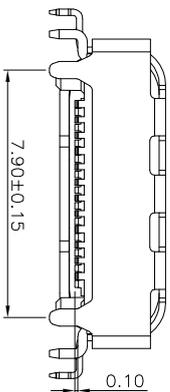
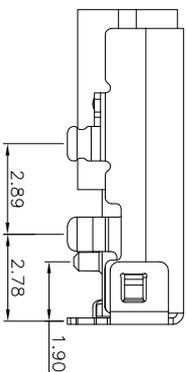
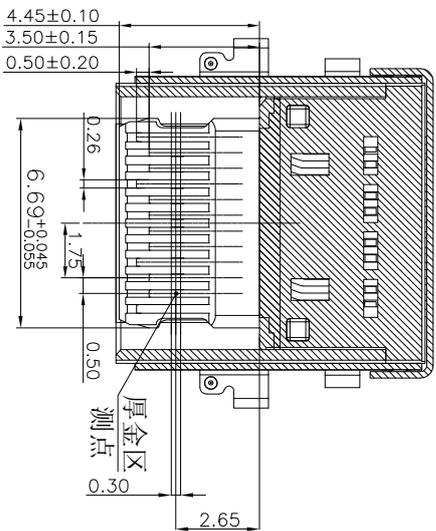
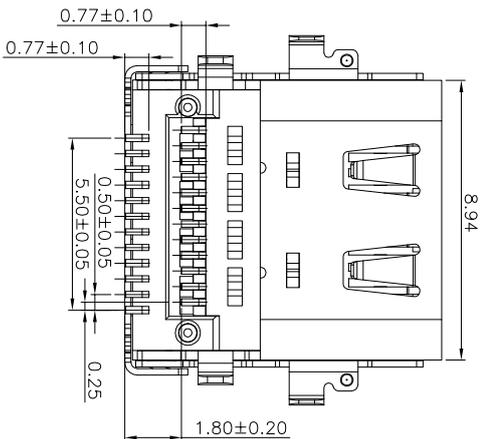
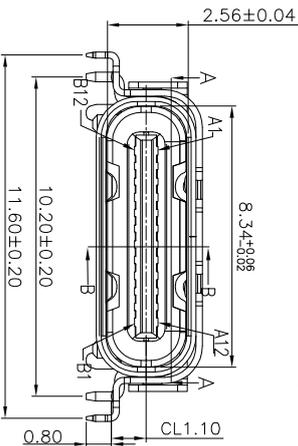
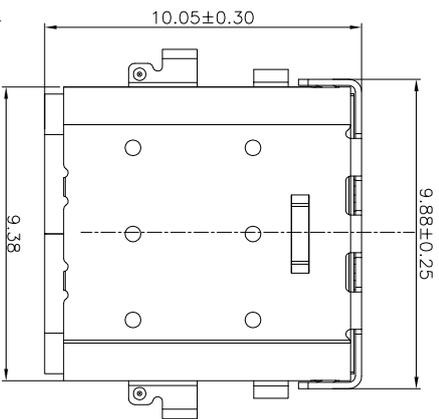


ROHS 客户图



目録 MAPX	变更内容 MODIFICATION	日期 DATE
A0	NEW	09-10-20

- NOTES:
- MATERIAL:
 - MOLDING: LCP UL94 V-0
 - CONTACT: COPPER ALLOY.
 - PLATED GOLD ON CONTACT AREA, G/F, GOLD ON SOLDER AREA, 50u" NICKEL PLATING OVER ALL.
 - OUTER SHELL: SUS304-H,T=0.30±0.03mm
50u" NICKEL SOLDER AREA TAIL
 - LATCH: SUS301, T=0.15±0.03mm
 - MECHANICAL:
 - INSERTION: 5~20N.
 - EXTRACTION: 8~20N.
 - DURABILITY: 10000 CYCLES
 - ELECTRICAL:
 - CURRENT: 5A MAX FOR VBUS;
 - 1.25A FOR GND, 0.25A FOR OTHER OTHER.
 - VOLTAGE: 20VDC MAX
 - WITHSTANDING VOLTAGE: 100V AC.
 - CONTACT RESISTANCE: 40mΩ MAX.
 - INSULATION RESISTANCE: 100MΩ MIN.
 - ENVIRONMENTAL
TEMPERATURE RANGE -40°C ~ +85°C

PART NO: 8001F-2400085-A

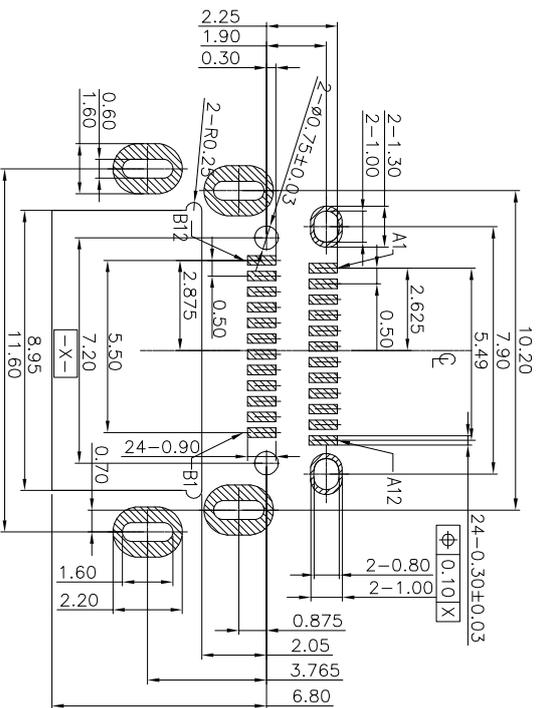
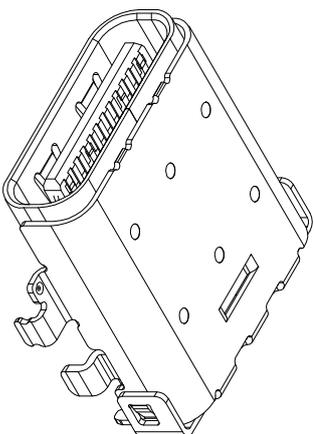
SERIE: TYPE C
 Receptacle
 serial number
 Variable

产品图

深圳市鑫风雷电子有限公司
 Shenzhen xinfenglei Electronics Co., Ltd

公差一览表 TOLERANCE UNLESS OTHERWISE SPECIFIED		单位 UNITS	比例 SCALE	日期 DATE	制图 DRAWING	审核 CHECK	核准 APPRO	产品料号 PRODUCT PART NO	产品名称 PRODUCT NO	角法 VIEW	版本 VER
X	±0.30	X'	±5'		王冠	雷永健	雷永健	8001F-2400085-A	TYPE C 24P母板沉板双壳结构: CH=1.1 L=10.06 脚长: 8	☉	A0
X	±0.25	X'	±2'		王冠	雷永健	雷永健			☐	
XX	±0.30	XX'	±1'		王冠	雷永健	雷永健			☐	
XXX	±0.15	XXX'	±0.5'	2009/10/20	王冠	雷永健	雷永健			☐	

RoHS 客户图



RECOMMEND P.C.B LAYOUT(COMPONENT SIDE)
TOLERANCE FOR PCB LAYOUT IS ± 0.05

USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair
A4	Vbus	Bus Power	B9	Vbus	Bus Power
A5	CC1	Configuration Channel	B8	SBU2	Sideband Use (SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A8	SBU1	Sideband Use (SBU)	B5	CC2	Configuration Channel
A9	Vbus	Bus Power	B4	Vbus	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	B1	GND	Ground return

目錄 MAPX	變更內容 MODIFICATION	日期 DATE
A0	NEW	09-10-20

产品图

深圳市鑫风雷电子有限公司
Shenzhen xinfenglei Electronics Co., Ltd

公差一览表 TOLERANCE UNLESS OTHERWISE		单位 UNITS		比例 SCALE		日期 DATE		制图 DRAWING		产品料号 PRODUCT PART NO.	
X	±0.30	X'	±5'	1:1	2009/10/20	王冠	8001F-2400085-A	审核 CHD	雷永健	产品名称 PRODUCT NO.	TYPE C 24PIN母板连接器 09-1.1 1-10.05 脚长: 8
XX	±0.25	X''	±2'			核准 APPD		角法 VIEW	版本 VER		
XXX	±0.15	XXX'	±0.5'							A0	